

THE COMMISSIONER FOR PATENTS
P.O.Box 1450, Alexandria, VA 22313-1450

Docket No. TS01-999

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: **SHI-CHI LIN**

For: **METHOD OF MANUFACTURING DIELECTRIC ISOLATED SILICON STRUCTURE**

16424 U.S. PRO
10/650601
08/28/03

Enclosed are:

3 sheets of drawing(s) - formal.

An assignment of the invention to **Taiwan Semiconductor Manufacturing Co.**

An associate power of attorney Applicant claims small entity status

Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

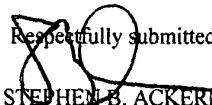
	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE				\$ 750.
TOTAL CLAIMS	20 -20=	0	x 18 =	\$ 0.
INDEP CLAIMS	3 -3=	0	x 84 =	\$ 0.
			SUB TOTAL	\$ 750.
			ASSIGNMENT	\$40.
			TOTAL	\$ 790.

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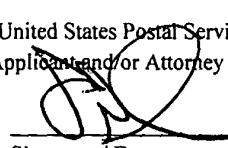
Respectfully submitted,

STEPHEN B. ACKERMAN, REG. NO. 37,761

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Name Stephen B. Ackerman, Reg. No. 37,761

Signature



Date

August 28, 2003

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

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